

In the Specification:

Please replace the paragraph beginning at page 7, line 21, with the following rewritten paragraph:

In general, neither the CPU nor the compiler can distinguish between local and global memory operations. Nor can the CPU or the compiler determine which global memory operation access must be ordered. It is therefore necessary for the programmer to indicate the ordering by use of special compiler directives which would cause the compiler to insert special assembly language instructions, ~~or by inserting special assembly language instructions~~. Such instructions explicitly indicate which write operations are to be placed in a specific order of operation and which write operations may occur arbitrarily.